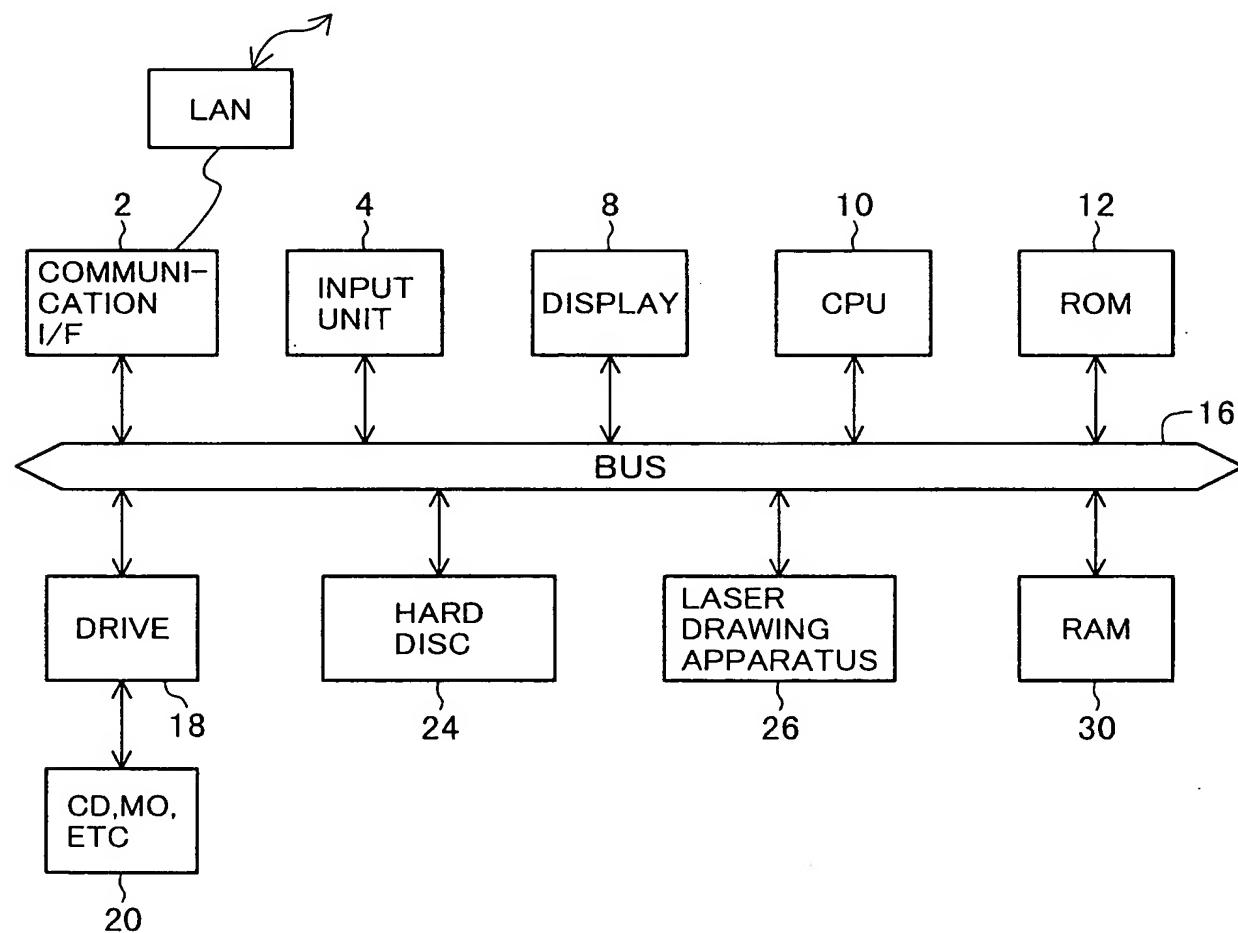


**FIG. 1**



**FIG.2**

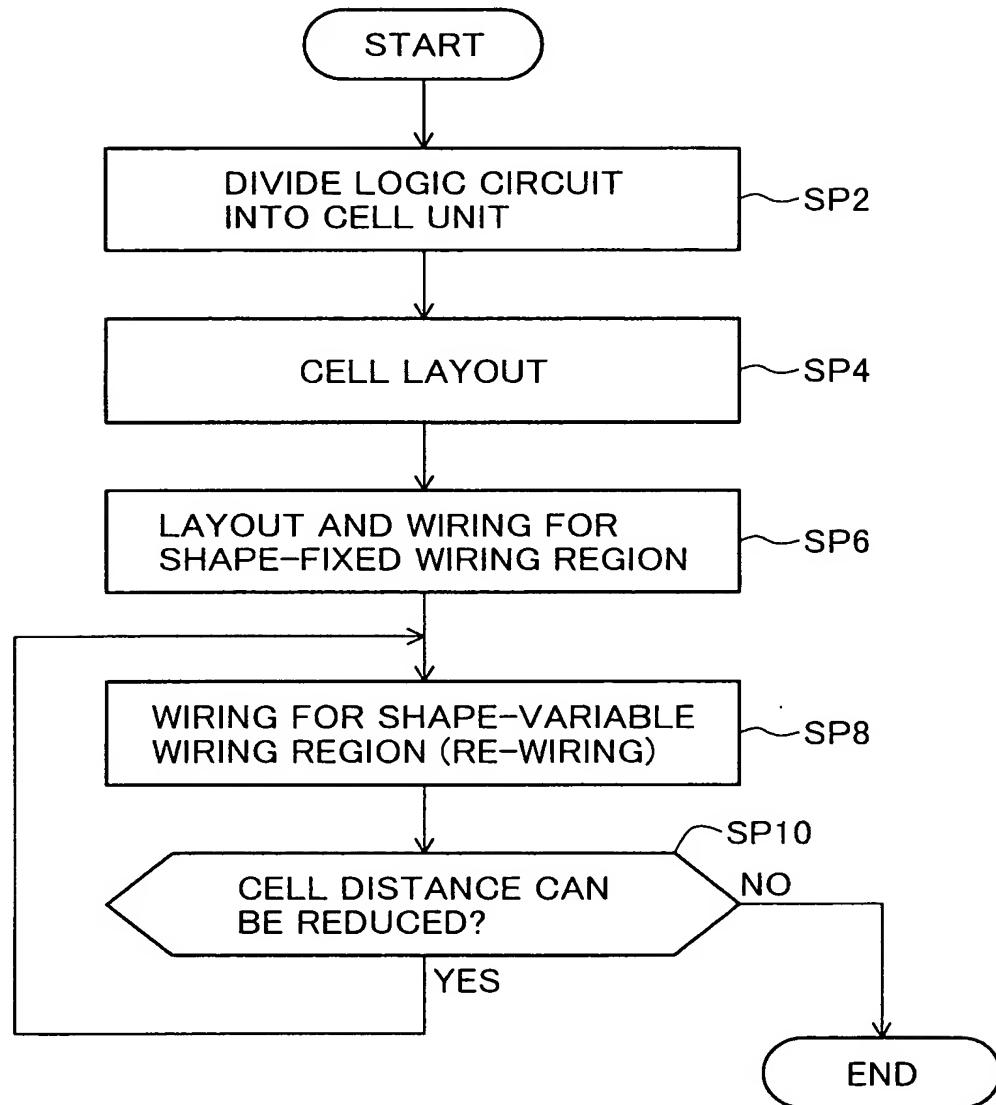


FIG. 3A

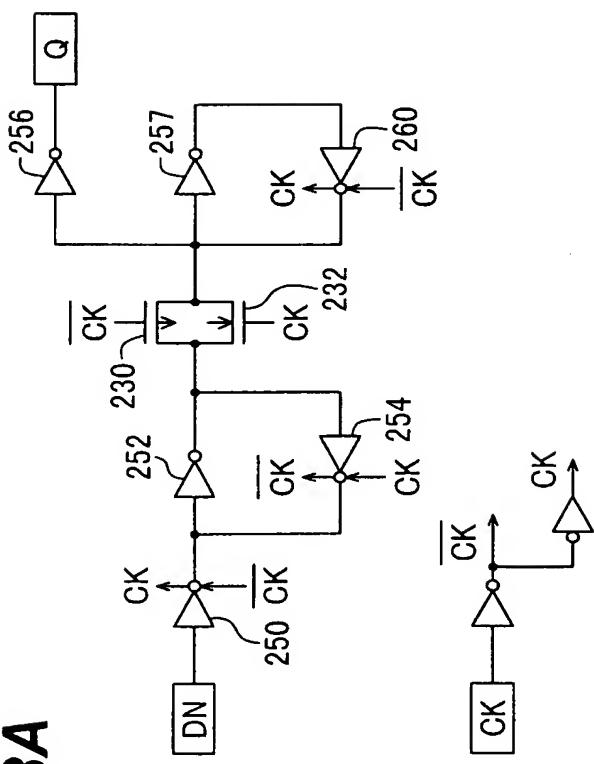
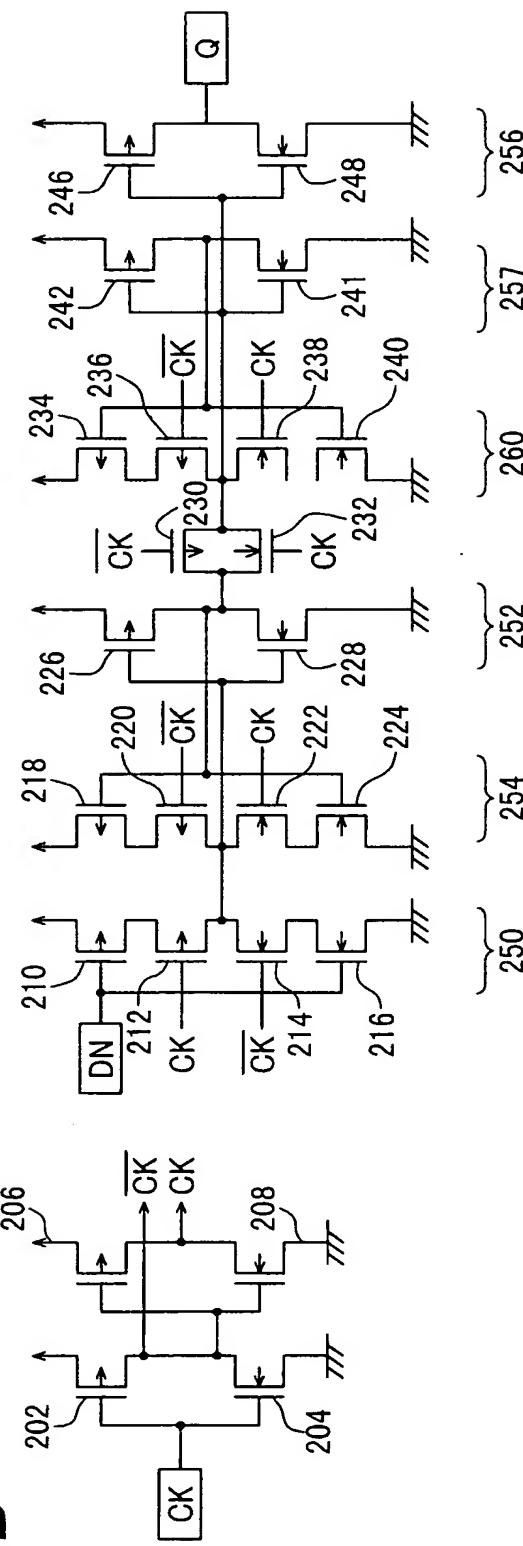
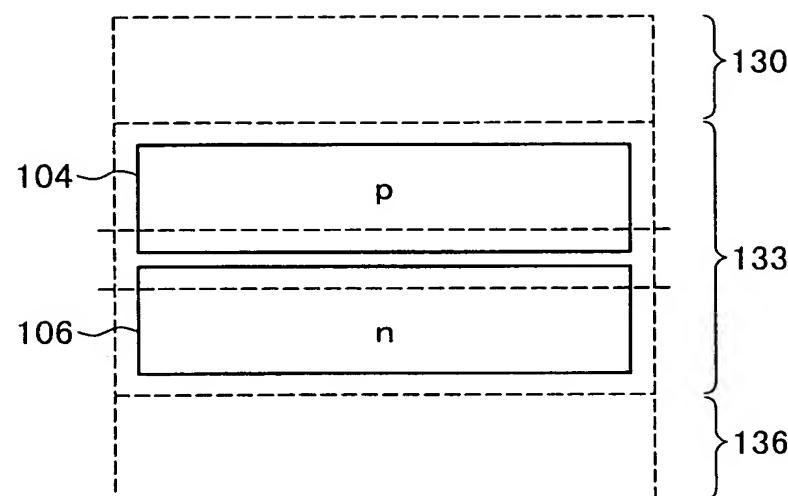
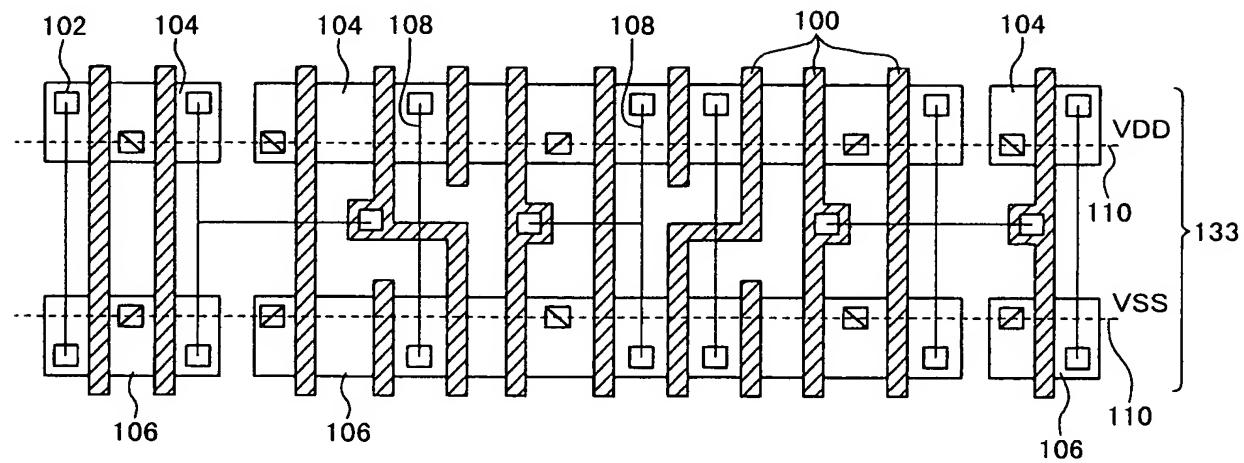
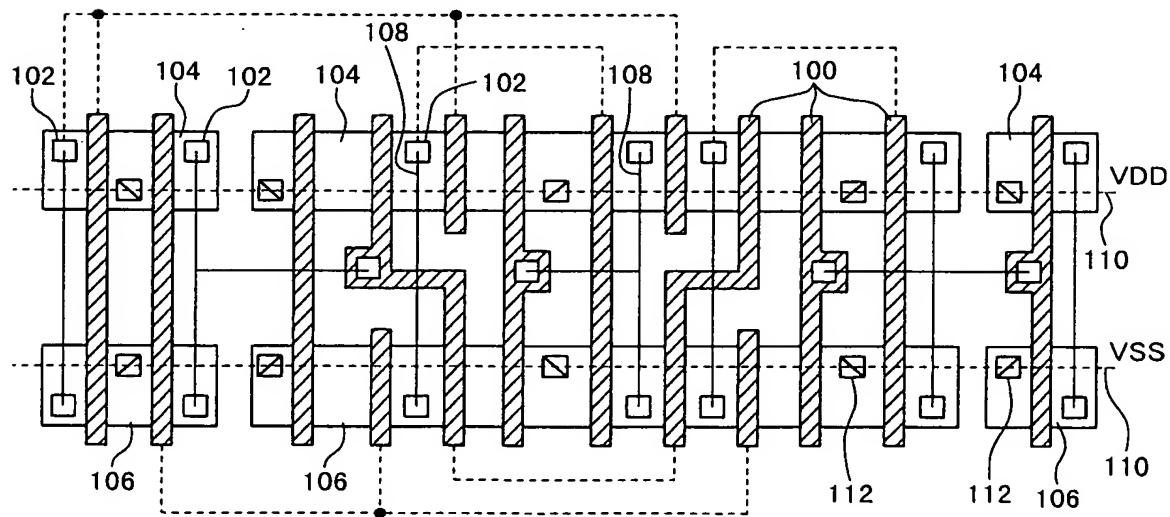


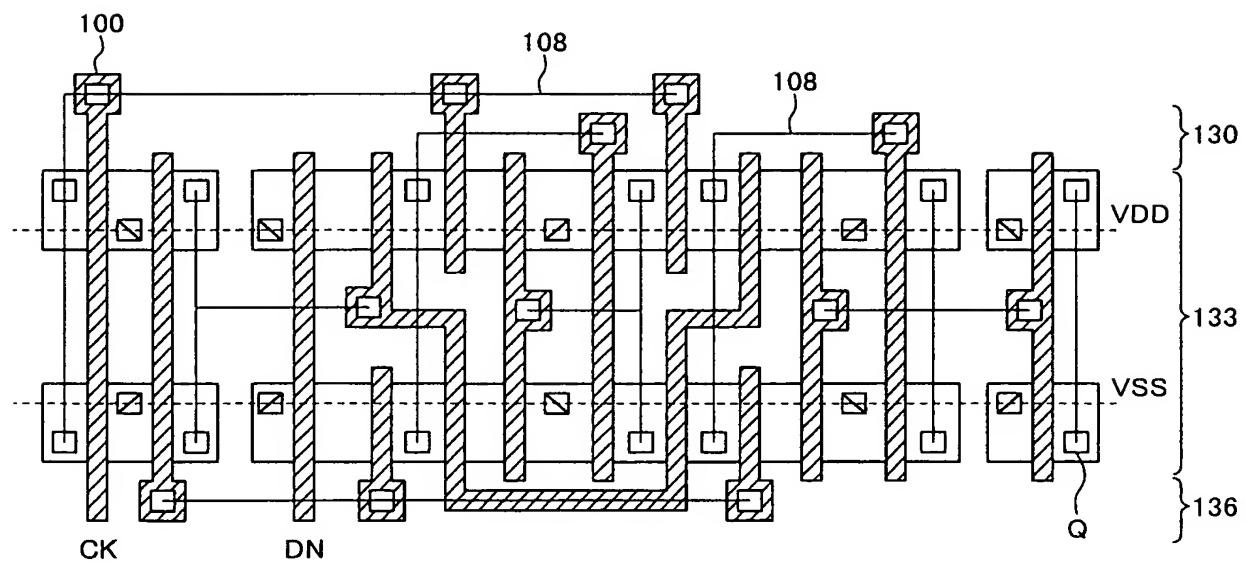
FIG. 3B



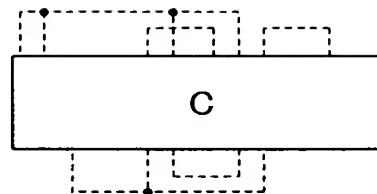
**FIG.4**

**FIG.5A****FIG.5B**

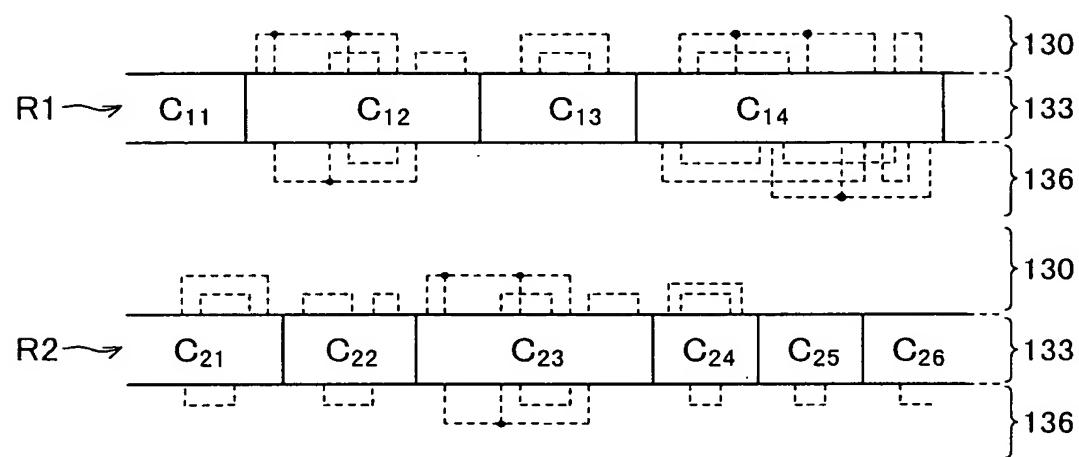
**FIG.5C**



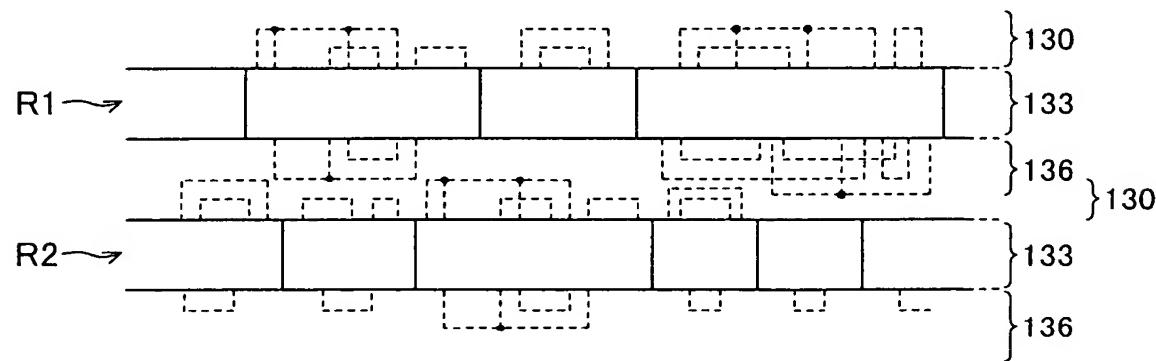
**FIG.5D**



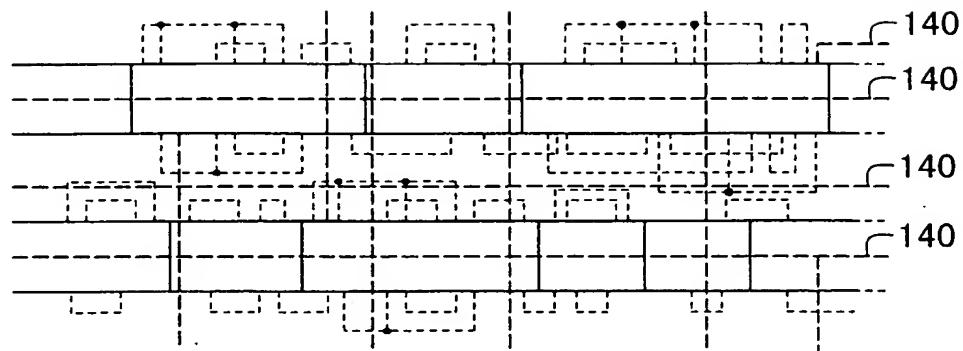
**FIG.5E**



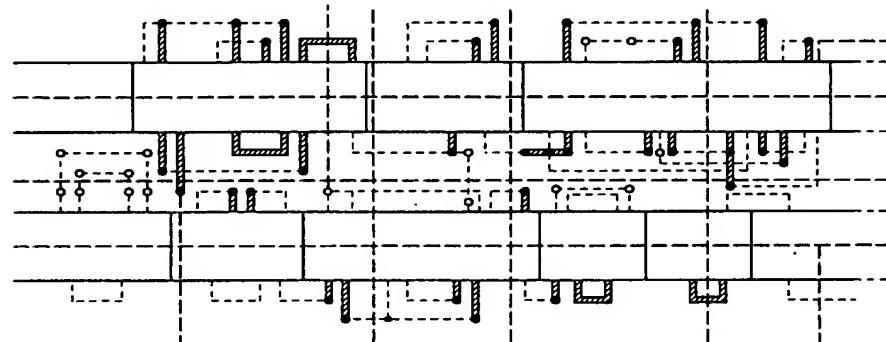
**FIG.5F**



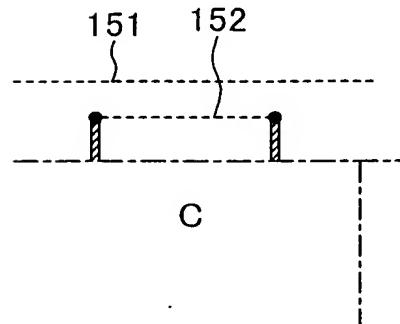
**FIG.5G**



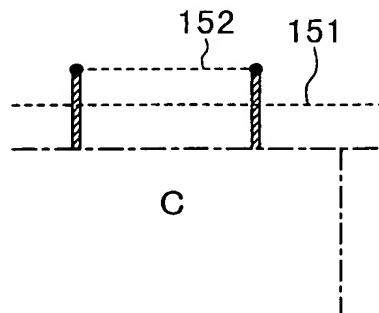
**FIG.5H**

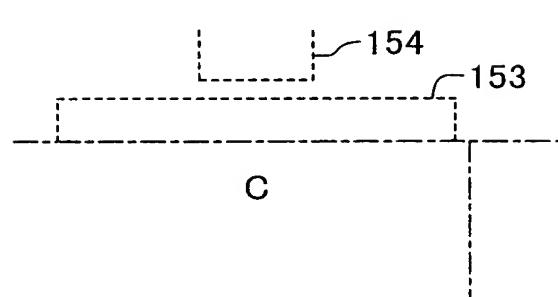
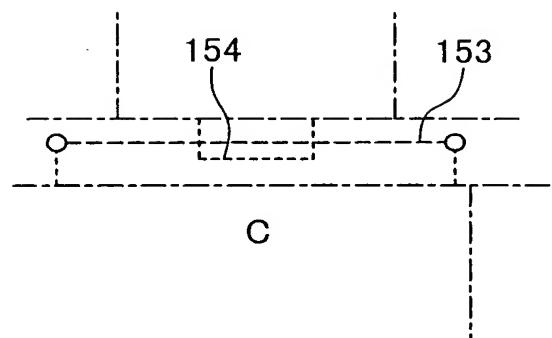
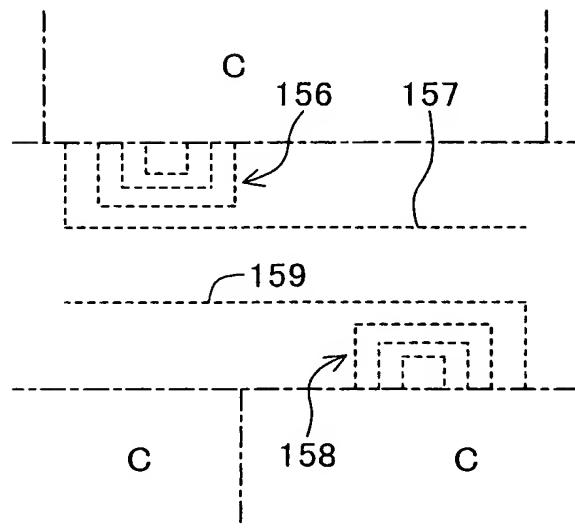
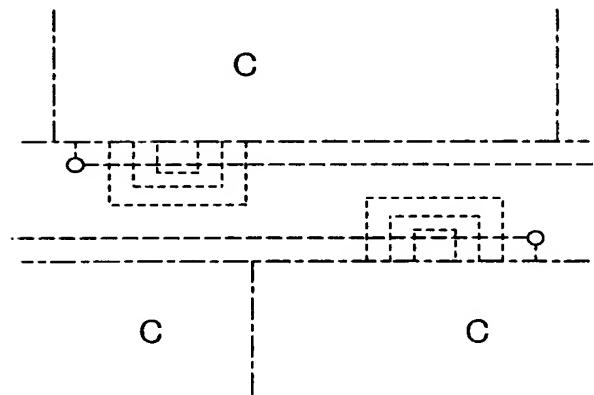


**FIG.5I**

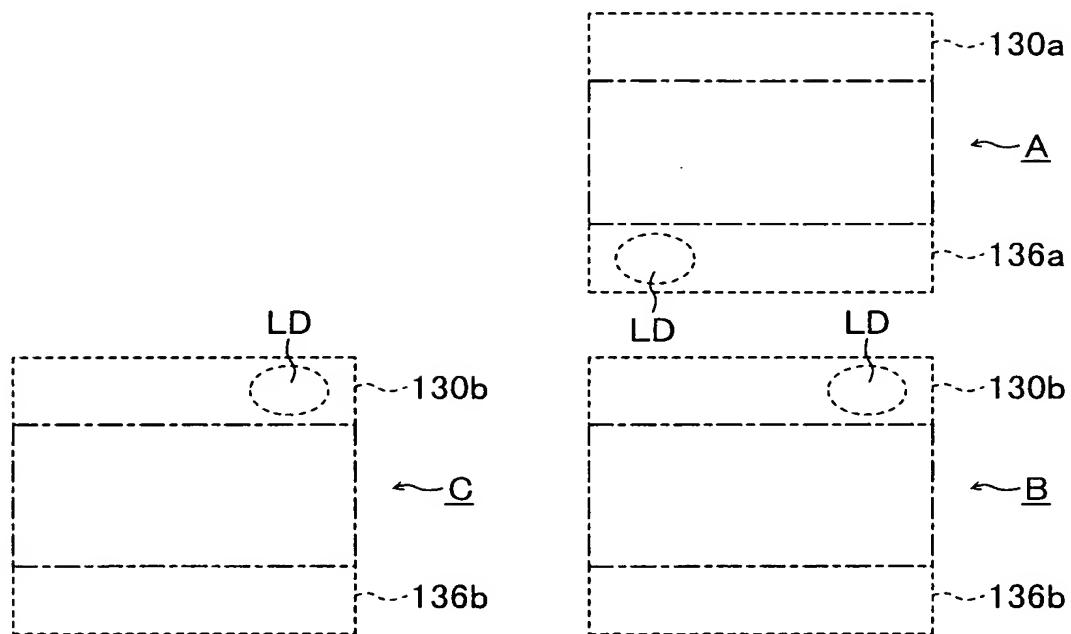


**FIG.5J**

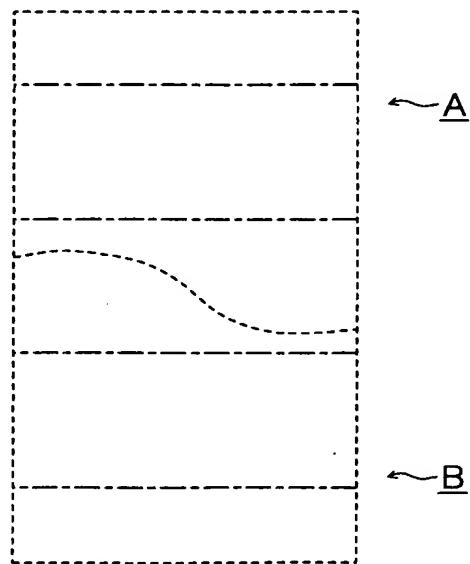


**FIG.5K****FIG.5L****FIG.5M****FIG.5N**

**FIG.6A**



**FIG.6B**



0306496

Yukichi ONO

SEMICONDUCTOR INTEGRATED CIRCUIT

DEVICE AND ITS MANUFACTURE...

Sheet 11 of 11

**FIG.6C**

